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| SERIAL NUMBER 10/621,055 | FILING DATE 07/15/2003 RULE | CLASS 327 | GROUP ART UNIT 2816 | ATTORNEY DOCKET NO. IDTL002US1-1826 |
|-----------------------------|-----------------------------------|--------------|------------------------|---|

APPLICANTS

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** CONTINUING DATA *****

This application is a CIP of 10/094,101 03/08/2002 PAT 6,664,838
 which claims benefit of 60/316,399 08/31/2001

yes

** FOREIGN APPLICATIONS *****

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 10/16/2003

| Foreign Priority claimed 35 USC 119 (a-d) conditions met | <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance | STATE OR COUNTRY CA | SHEETS DRAWING 16 | TOTAL CLAIMS 46 | INDEPENDENT CLAIMS 4 |
|--|---|---------------------------|-------------------------|-----------------------|----------------------------|
| Verified and Acknowledged Examiner's Signature | Initials | | | | |

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TITLE

Clock processing logic and method for determining clock signal characteristics in reference voltage and
 temperature varying environments

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